|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| Instruction | 1 | 2 | 3 | 4 | 5 | 6 | Regdist1 | Regdist2 | Jump | Branch | Memtoreg1 | Memtoreg2 | Memread | Aluop1 | Aluop2 | Memwrite | Alusrc | Regwrite |
| Add/nor/sll/  slt/jr | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 1 |
| J | 0 | 0 | 0 | 0 | 1 | 0 | x | x | 1 | 0 | x | x | 0 | x | x | 0 | x | 0 |
| Jal | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 1 | 0 | 1 | 0 | 0 | x | x | 0 | x | 1 |
| Beq | 0 | 0 | 0 | 1 | 0 | 0 | x | x | 0 | 1 | x | x | 0 | 0 | 1 | 0 | 0 | 0 |
| Addi | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 |
| Slti | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 1 |
| Lb | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 1 | 1 |
| Lbu | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 1 | 1 |
| Lw | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 1 | 1 |
| Sb | 1 | 0 | 1 | 0 | 0 | 0 | x | x | 0 | 0 | x | x | 0 | 0 | 0 | 1 | 1 | 0 |
| Sw | 1 | 0 | 1 | 0 | 1 | 1 | x | x | 0 | 0 | x | x | 0 | 0 | 0 | 1 | 1 | 0 |